

APPLICATION NO. 09/928767	ORIG/PRIOR	CLASS <del>885</del> 225	SUBCLASS 492,040	ART UNIT <del>2018</del> 2024	EXAMINER FUREMIN, J
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TITLE Molded memory module and method of making the module absent a substrate support					

PTO-2040  
12/89

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) _____ (Date)			<b>NOTICE OF ALLOWANCE MAILED</b>	
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				Amount Due	Date Paid
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